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REPLY UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE **EXAMINING GROUP 2185**

> **PATENT** 5500-88800/TT5375

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

§

Application No.:

10/676,437

Filed:

October 1, 2003

15128538801

Inventors:

Mitchell Alsup

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System and Method for

Handling Exceptional Instructions in a Trace

Cache Based Processor

Examiner:

Kim, Hong Chong

Group/Art Unit:

2185

Atty. Dkt. No:

5500-88800

I hereby certify that this correspondence is being faceimile transmitted to the U.S. Patent and Trademark Office (Fex No. (571) 273-8300), on the date indicated below.

Robert C. Kowert Printed Name

July 30, 2006

RESPONSE TO FINAL ACTION OF JULY 12, 2006

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Action of July 12, 2006, to further highlight why the application is in condition for allowance.